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John R. Long received the B.Sc.-E.E. from the University of Calgary in 1984, and the M.Eng. and Ph.D. degrees in Electronics Engineering from Carleton University in 1992 and 1996, respectively. He was employed for 10 years by Bell-Northern Research Ltd., Ottawa, Canada designing GaAs ASICs for Gbit/s systems, and has taught at the University of Toronto (1996-2002) and the Delft University of Technology (2002-2014) in the Netherlands. He is currently a professor at the University of Waterloo in Canada. His research interests include: low-power transceiver circuitry for highly-integrated radio applications, and electronics design for high-speed data communications systems.

Professor Long is a recipient of the NSERC Doctoral Prize, Douglas R. Colton and Governor General's Medals for research excellence, and Best Paper Awards, including: ISSCC in 2000 and 2007, IEEE-BCTM 2003, and the IEEE-RFIC Symposium in 2006 and 2011. He is a member of the ESSCIRC technical program committee and has served on the technical program committees for the ISSCC (RF subcommittee chair), BCTM, EuMW, and ICUWB conferences. He was co-chair of the European microwave IC conference in 2008 and 2012. Associate Editor of the IEEE Journal of Solid-State Circuits, and General Chair of the IEEE Bipolar/BiCMOS Circuits and Technology Meeting. He is currently a Distinguished Lecturer for the IEEE Solid-State Circuits Society and Editor-in-Chief of the new IEEE virtual journal on RFICs.